



JAIDEV EDUCATION SOCIETY'S
J D COLLEGE OF ENGINEERING AND MANAGEMENT
KATOL ROAD, NAGPUR

Website: www.jdcoem.ac.in E-mail: info@jdcoem.ac.in
(An Autonomous Institute, with NAAC "A" Grade)
Affiliated to DBATU, RTMNU & MSBTE Mumbai



Examination Section

VISION

To win the trust of all stakeholders in conducting the assessment and evaluation.

MISSION

1. To frame and adopt procedure for various works involved in accountability.
2. To utilize the resources as per expertise of individual and maintaining good quality and standard of assessment work.
3. To ensure that the student participate in assessment process enthusiastically.

OFFICE OF CONTROLLER OF EXAMINATIONS

Time Table of M. Tech. II-Sem Summer 2026 (Regular) Theory Examination

Time: 09.30 AM to 12.00 PM

Date: 30-03-2026

Branch / Day & Date of Exam	Tuesday	Thursday	Tuesday	Thursday	Saturday	Tuesday
	21-04-2026	23-04-2026	28-04-2026	30-04-2026	02-05-2026	05-05-2026
Computer Science & Engineering	Data Science	Intrusion Detection System	Program Elective-II	Software Architecture	Open Elective-II	Strategic Management – The Competitive Edge
Electronics Engineering	Advanced Digital Signal Processing	Advanced Embedded System Design	Professional Elective-II	Blockchain Technology	Open Elective-II	Strategic management for competitive advantage
Structural Engineering (CE)	Finite Element Method	Foundation Design	Program Elective-II	Disaster Management and Mitigation	Open Elective-II	Strategic Management – The Competitive Edge
Electrical Power Systems (EE)	Power System Modeling	Advanced Microcontroller	Professional Elective-II	Machine Learning	Open Elective-II	Strategic management for competitive advantage

Practical Examination

Time: 10.30 AM to 04.00 PM

Branch / Day & Date of Exam	Thursday	Friday
	16-04-2026	17-04-2026
Computer Science & Engineering	Data Science Lab	Intrusion Detection System Lab
Electronics Engineering	Advanced Digital signal Processing	Advanced Embedded System Design
Structural Engineering (CE)	Finite Element Method Lab	Foundation Design Lab
Electrical Power Systems (EE)	Power System Modeling Lab	Advanced Microcontroller Lab

Copy to:

1. Hon'ble Directors, JES
2. All Dean's / H.O.D.'s / SH's for necessary action.

Dy. Controller of Examinations

Controller of Examinations

Principal

