



Education to Eternity

**JAIDEV EDUCATION SOCIETY'S  
JD COLLEGE OF ENGINEERING AND MANAGEMENT  
KATOL ROAD, NAGPUR**

Website: [www.jdcoem.ac.in](http://www.jdcoem.ac.in) E-mail: [info@jdcoem.ac.in](mailto:info@jdcoem.ac.in)  
(An Autonomous Institute, with NAAC "A" Grade)  
Affiliated to DBATU, RTMNU & MSBTE Mumbai



**Examination Section**

**VISION**

To win the trust of all stakeholders in conducting the assessment and evaluation.

**MISSION**

1. To frame and adopt procedure for various works involved in accountability.
2. To utilize the resources as per expertise of individual and maintaining good quality and standard of assessment work.
3. To ensure that the student participate in assessment process enthusiastically.

**OFFICE OF CONTROLLER OF EXAMINATIONS**

**Time Table of M. Tech I-Sem Winter 2025 (Supplementary) Theory Examination**

Time: 10.30 AM to 01.00 OR 01.30 PM

Exam Batch 2019-20 to 2024-25

Date: 09-09-2025

Branch / Day & Date of Exam	Wednesday	Thursday	Friday	Tuesday	Wednesday	Thursday
	17-09-2025	18-09-2025	19-09-2025	23-09-2025	24-09-2025	25-09-2025
<b>Computer Science &amp; Engineering</b>	Computer Algorithms	Advanced Operating System	Elective-I	High Performance Computing <b>OR</b> Advanced Computer Network	Open Elective-I <b>OR</b> Elective II	Graphics Designing <b>OR</b> Communication Skill
<b>Electronics &amp; Telecommunication Engineering</b>	High Speed Semiconductor Devices & Circuits	VLSI System Design	Professional Elective-I <b>OR</b> Embedded System Design	Computational Methods	Open Elective-I <b>OR</b> Elective II	Research Methodology




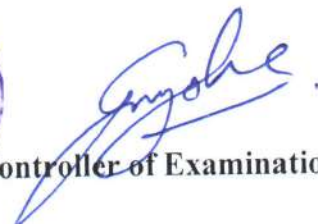
**Practical Examination**


Time: 02.00 PM to 05.00 PM

Branch / Day & Date of Exam	Wednesday	Thursday	Friday
	17-09-2025	18-09-2025	19-09-2025
<b>Computer Science &amp; Engineering</b>	Computer Algorithms	Advanced Operating Systems	Graphics Designing
<b>Electronics &amp; Telecommunication Engineering</b>	PG Lab <b>OR</b> Embedded System Design	VLSI System Design	Graphic Design

Copy to:

1. Hon'ble Directors, JES
2. All Dean's / H.O.D.'s / SH's for necessary action.

Dy. Controller of Examinations    Controller of Examinations 

Principal 

**Controller of Examinations  
JDCOEM, Nagpur**